

Project Assignment 1

Due Thursday, Oct 21, 1999

Some circuits in the KSU696 IC are analog, some are digital, and some are RF (high frequency analog). To maximize each company's probability of producing a complete working chip, and to minimize everyone's workload, it is important that individuals be assigned to sections of the design for which they have the most background, skills, and interest.

Your instructor will play the role of system engineer and CTO (chief technical officer), partitioning the block diagram and assigning individuals to particular teams. It is anticipated that the teams will be composed as follows:

- Analog circuit engineers -- Design analog baseband circuits, including stereo matrix and low frequency VCO
- Digital circuit engineers -- Design digital circuits, including serial-to-parallel converter, frequency dividers, and PFD logic
- RF circuit engineers -- Design high frequency VCO, mixer, and RF harmonic filter
- Test engineer -- Coordinate design and layout to maximize ease of testing, and develop test plan and test jig.

To assist your instructor in his role, you should prepare a one page resume that includes the following items:

- A brief statement of your career objectives (what you think you want to do when you graduate) and your first, second, and third choice for teams you would like to work on
- A list of course you have taken which you think will help you in design (e.g. 241, 431, 502, 525, 526, etc.)*
- Any relevant work experience or other information you think might be helpful